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ABSTRACT OF THE DISCLOSURE

An apparatus comprising a first circuit, a second circuit and a third circuit. The first circuit may be configured to communicate as a host through a first interface. The second circuit may be configured to communicate through a second interface as a host when in a first mode and as a peripheral when in a second mode. The third circuit may be configured to (i) control the first and the second circuits and (ii) transfer information between the first and the second circuits.